

**UTILITY
PATENT APPLICATION
TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.70)

Attorney Docket No. 202887US2
 First Inventor or Application Identifier Yuichi HIRANO, et al.
 Title SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING

Assignee Name: MITSUBISHI DENKI KABUSHIKI KAISHA
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A
 JC973 U.S. PTO 09/178104
 02/07/01

APPLICATION ELEMENTS
 See MPEP chapter 600 concerning utility patent application contents

1. Fee Transmittal Form (e.g. PTO/SB/17)
 (Submit an original and a duplicate for fee processing)

Specification Total Sheets 34

Drawing(s) (35 U.S.C. 113) Total Sheets 22
 Formats

2. Oath or Declaration Total Pages 4

- Newly executed (original)
- Copy from a prior application (37 C.F.R. §1.63(d))
 (for continuation / divisional w/ box 17 completed)
 - DELETION OF INVENTOR(S)
 Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).
- CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)
- Nucleotide and/or Amino Acid Sequence Submission
 (if applicable, all necessary)
 - Computer Readable Form (CRF)
 - Specification or Sequence Listing on:
 - CD-ROM or CD-R (2 copies); or
 - Paper
 - Statements verifying identity of above copies

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:

Continuation Divisional Continuation-in-part (CIP) of prior application no.:

Prior application information: Examiner:

Group Art Unit:

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

18. Amend the specification by inserting before the first line the sentence:

This application is a Continuation Division Continuation-in-part (CIP)
 of application Serial No. Filed on
 Which was published in English
 Which was not published in English
 This application claims priority of provisional application Serial No. Filed

19. CORRESPONDENCE ADDRESS



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Signature:		Date:	2/7/01
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Registration Number 21,124

Docket No. 202887US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Yuichi HIRANO, et al.

SERIAL NO: New Application

FILING DATE: Herewith

FOR: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING SAME

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	13 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	3 - 3 =	0	x \$80 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$270 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
			BASIC FEE	\$710.00
			TOTAL OF ABOVE CALCULATIONS	\$710.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
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Please charge Deposit Account No. 15-0030 in the amount of A duplicate copy of this sheet is enclosed.
 A check in the amount of \$710.00 to cover the filing fee is enclosed.
 The Commissioner is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030.
A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Date: 2/7/01

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